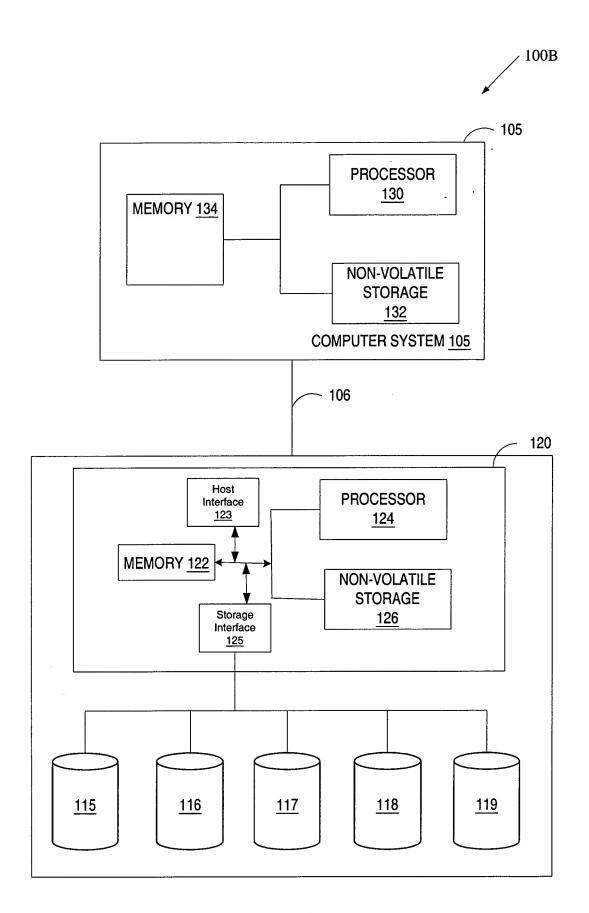


Figure 1A



Figur 1B

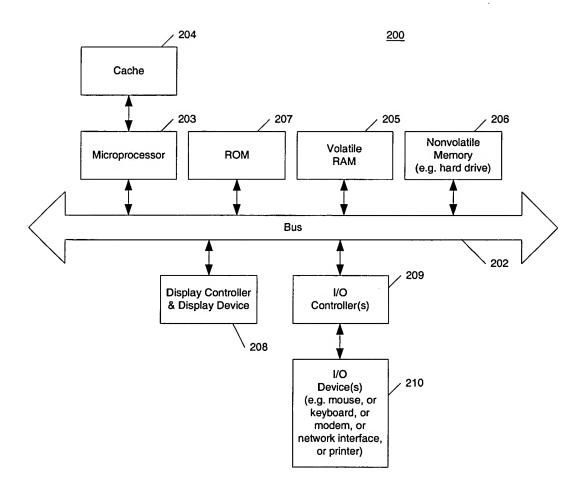


Figure 2

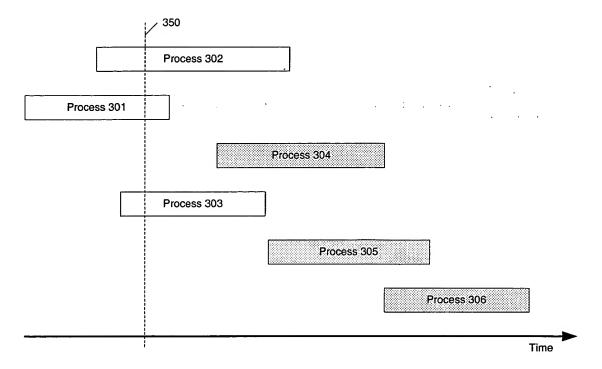
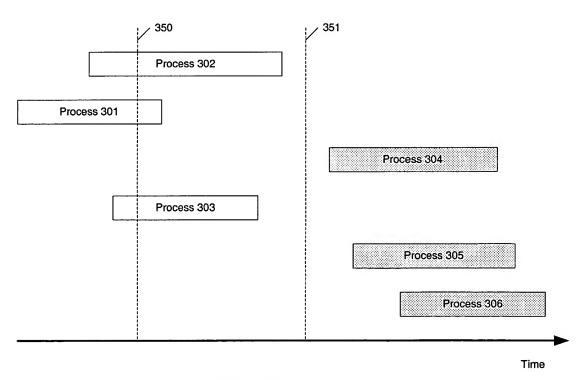


Figure 3A



Figur 3B

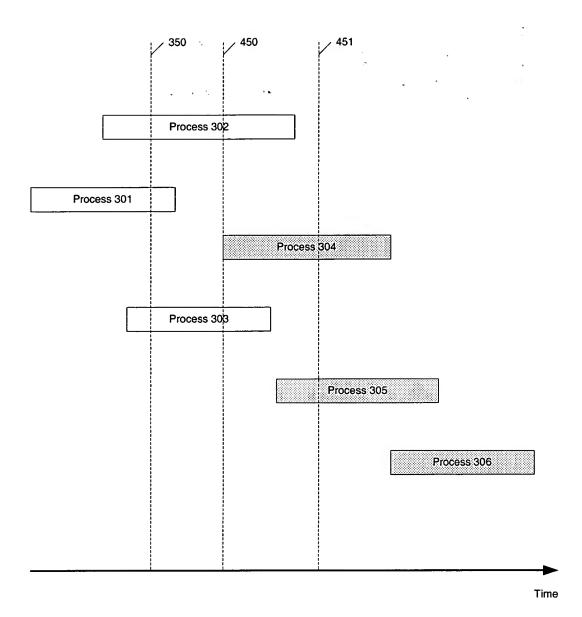


Figure 3C

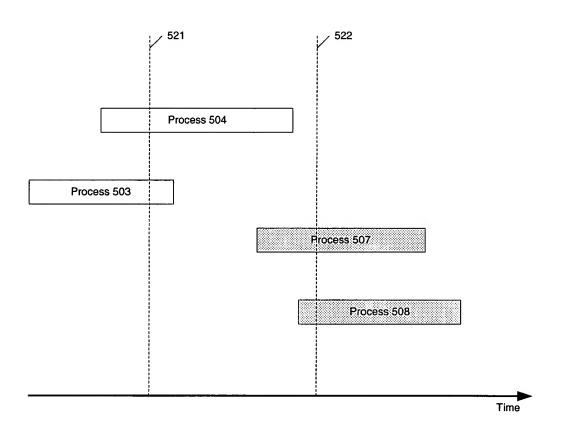


Figure 4

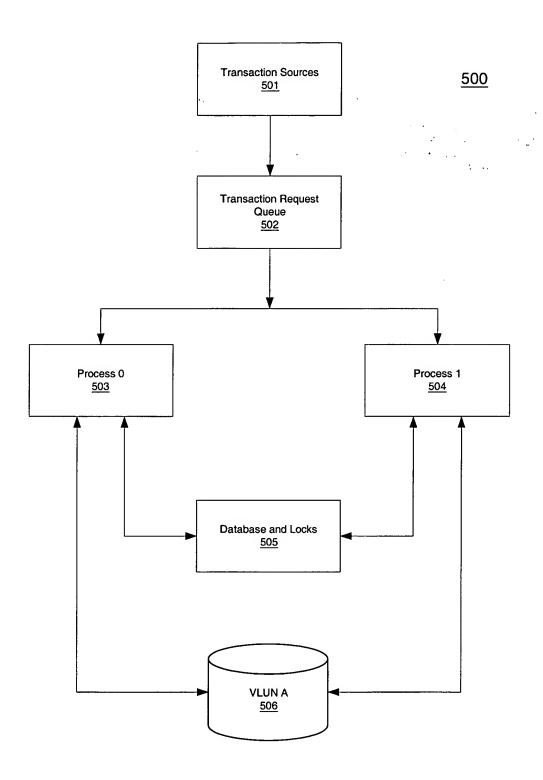
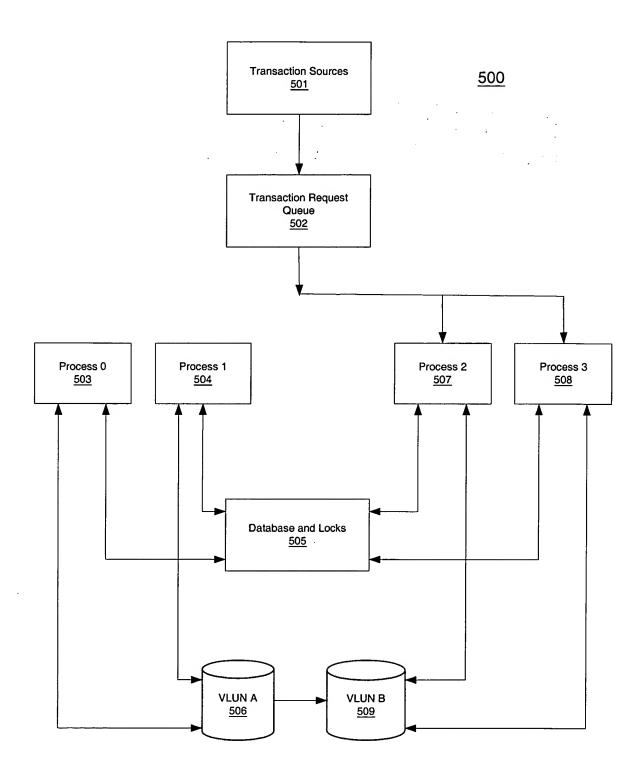
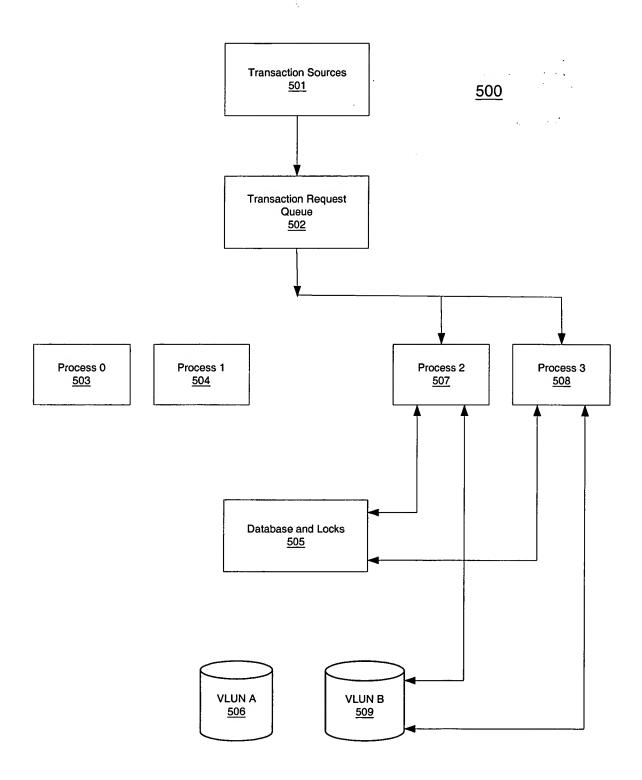


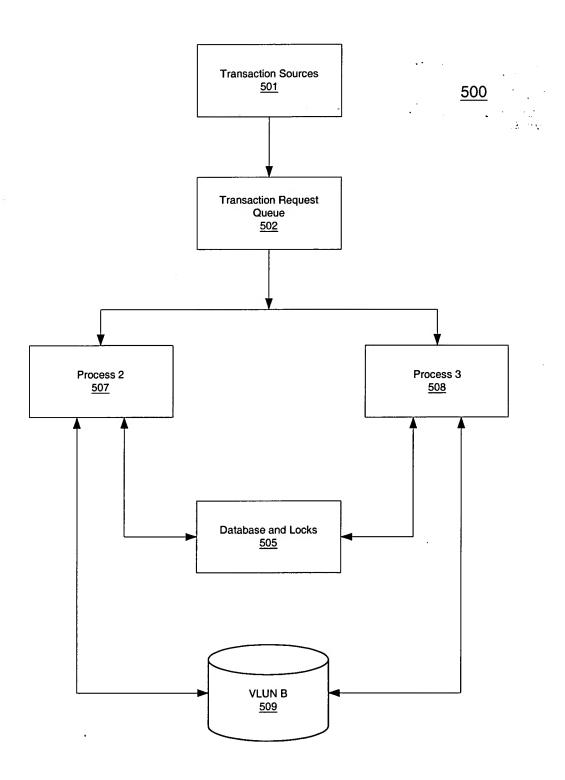
Figure 5A



Figur 5B



Figur 5C



Figur 5D

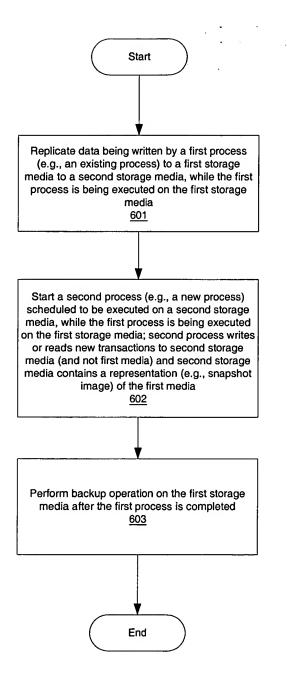
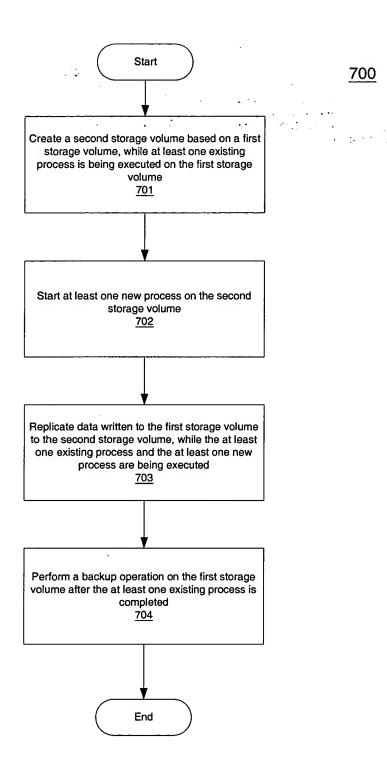


Figure 6



Figur 7

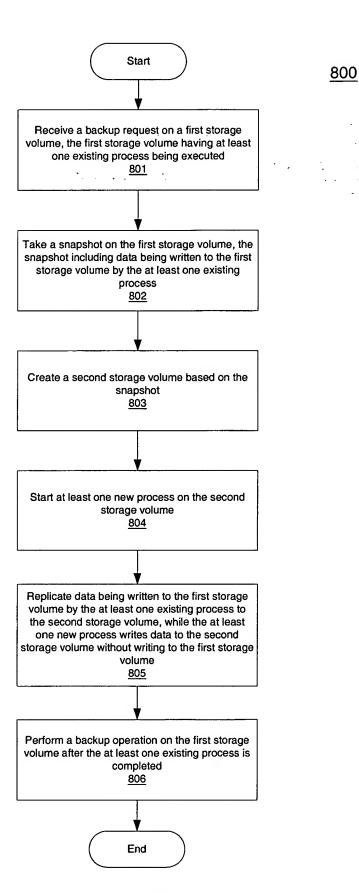


Figure 8

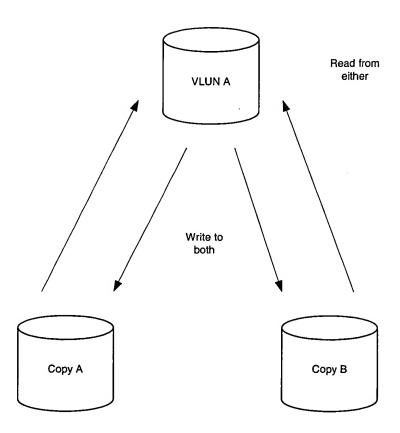


Figure 9A

VLUN A VLUN B

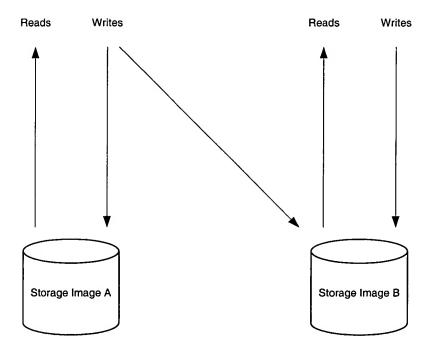


Figure 9B

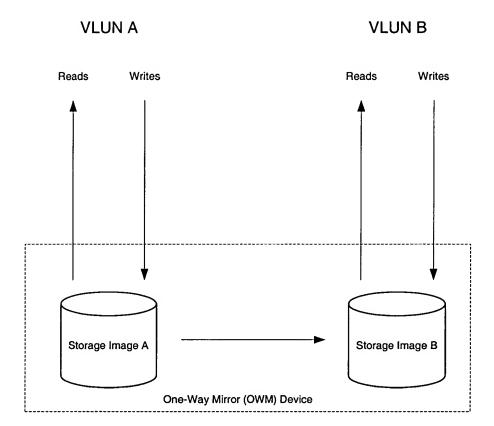


Figure 10

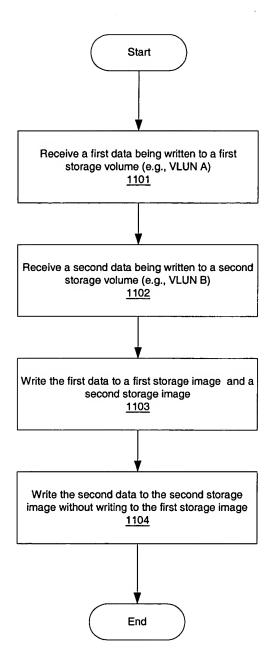


Figure 11

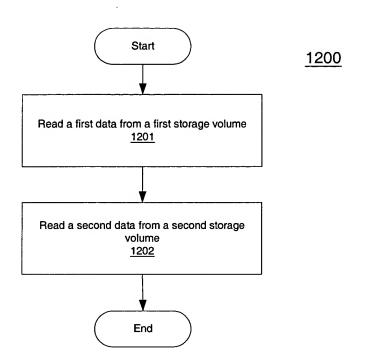
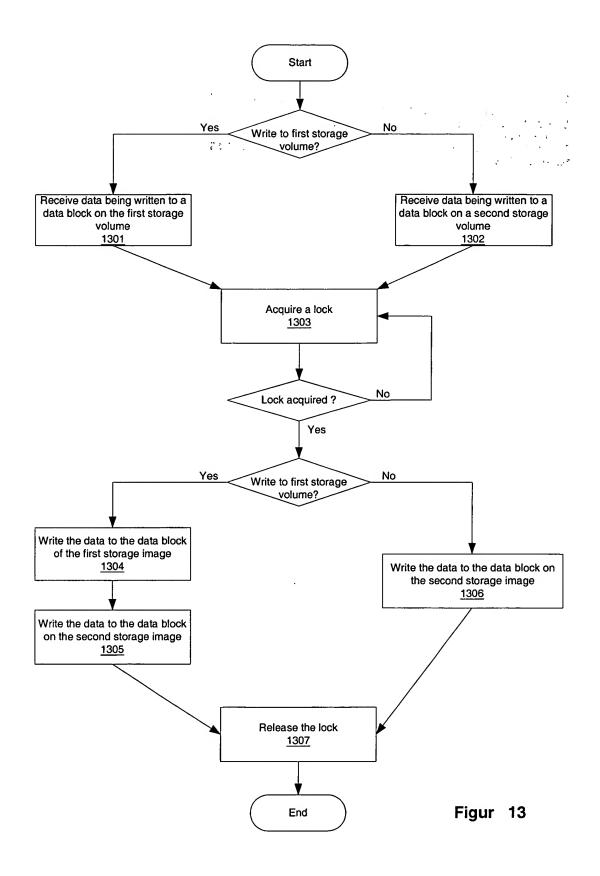


Figure 12



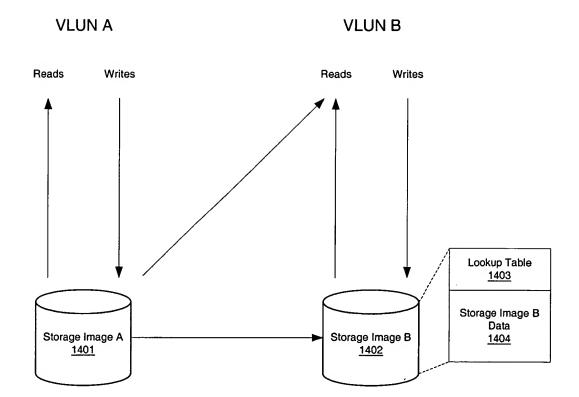


Figure 14

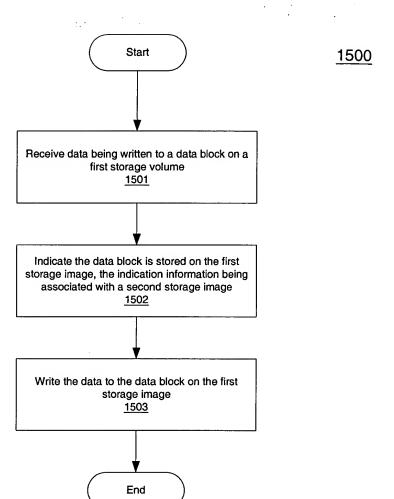


Figure 15

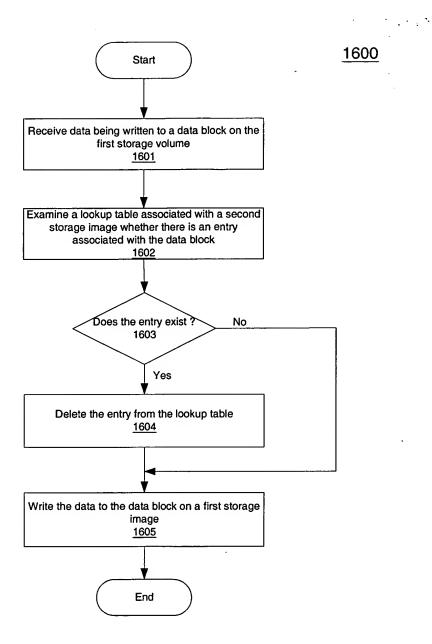


Figure 16

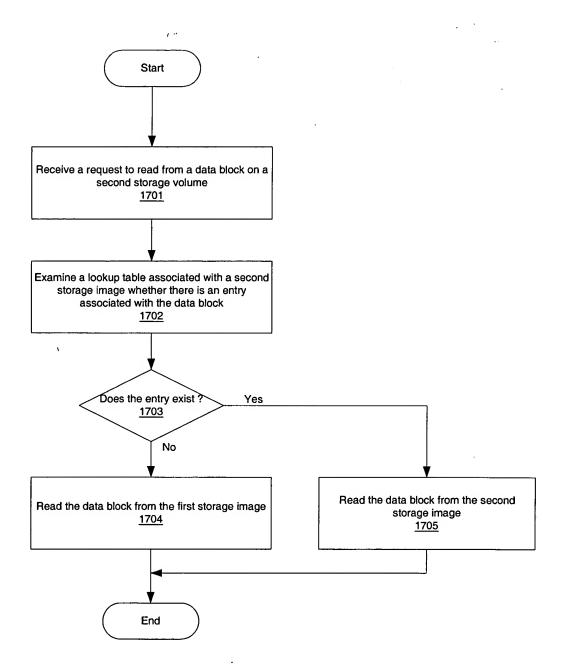


Figure 17

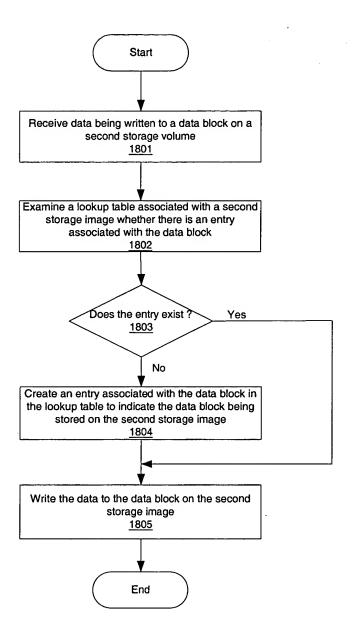
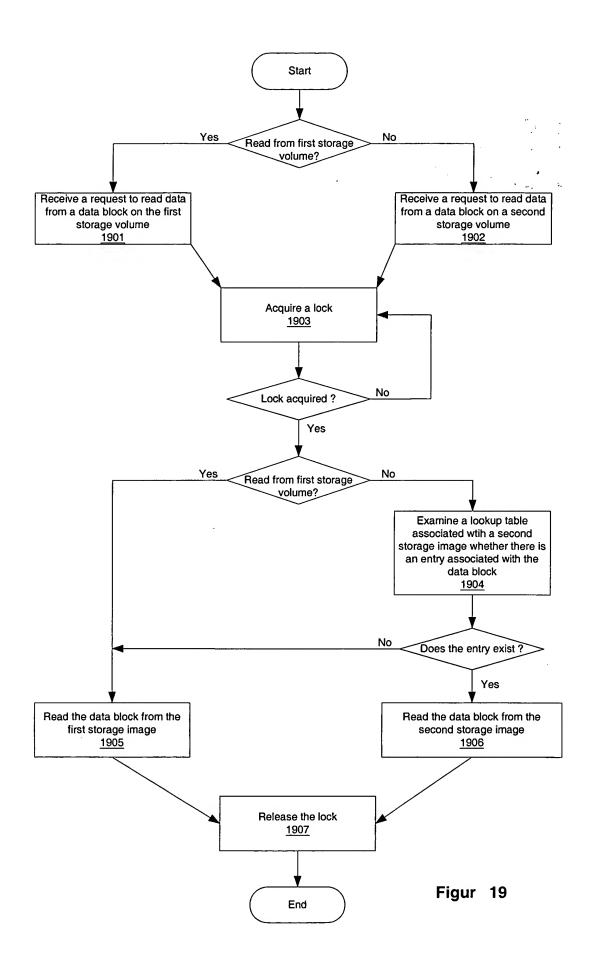
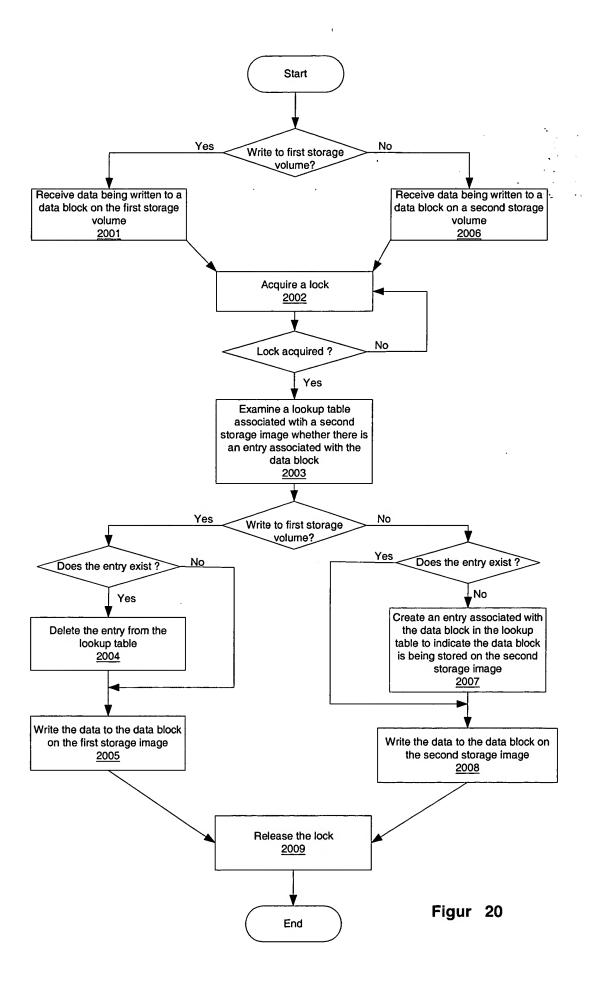


Figure 18





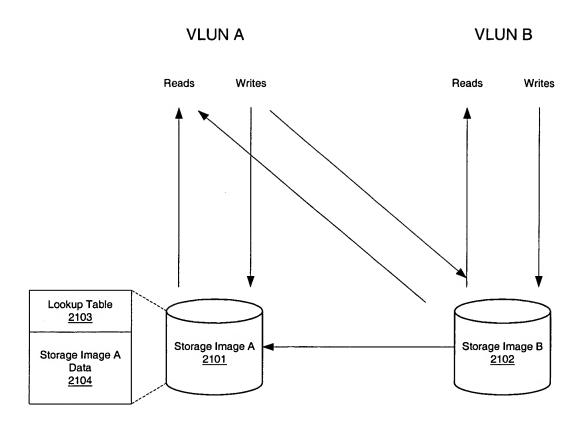


Figure 21

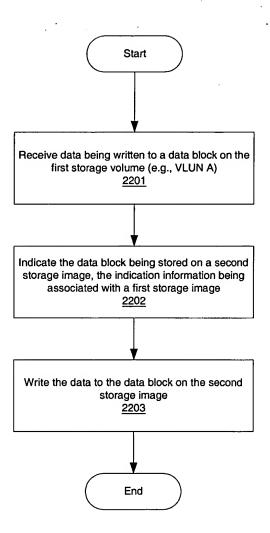


Figure 22

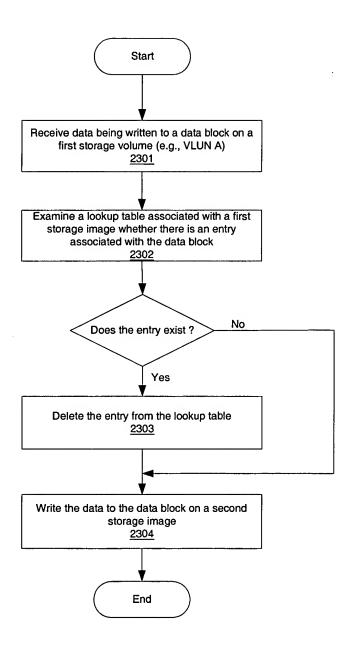


Figure 23

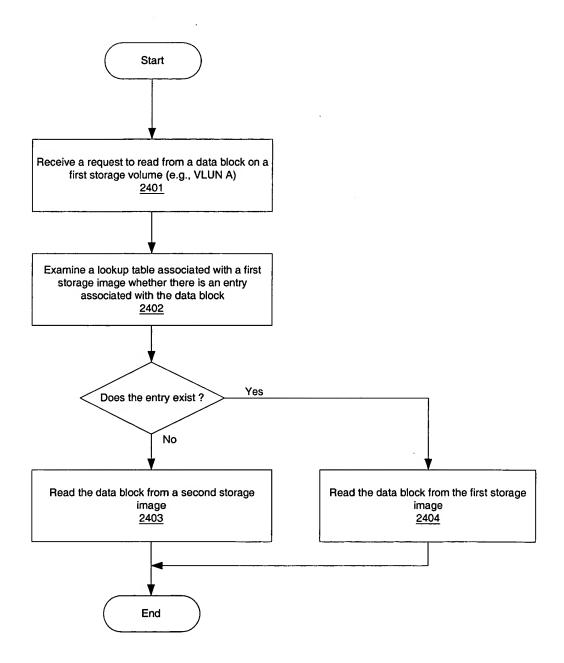


Figure 24

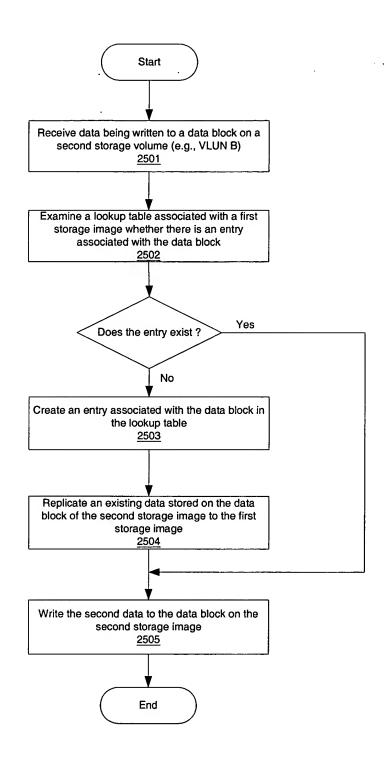


Figure 25

